

### **In the Specification**

Please insert the following paragraph before the first paragraph on the first page of the specification:

-- This application is a National Phase Application of International Application No. PCT/JP2005/016895, filed September 14, 2005, which claims the benefit under 35 U.S.C. 119 (a-e) of Japanese Application No. 2004-272642 filed September 17, 2004, which is herein incorporated by reference.--

At page 14, paragraph 6, starting on line 21, please change to read as follows:

--FIG. 11 is a block diagram showing a configuration of the entertainment apparatus 10. As shown in this drawing, the entertainment apparatus 10 includes a main processing unit (MPU) 100, a graphics processor (GP) 110, the input/output sub-processor (IOP) 120, a CD/DVD decoder 130, a sound processing unit (SPU) 140, read-only memory (OSROM) 150, main memory 160, and IOP memory 170.--